

Fig. 1



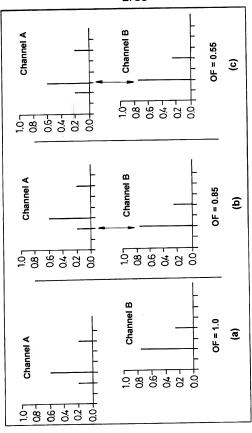
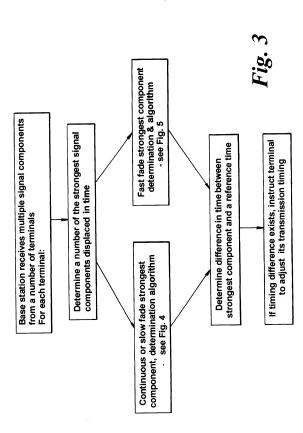


Fig. 2

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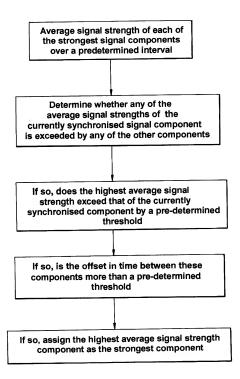


Fig. 4

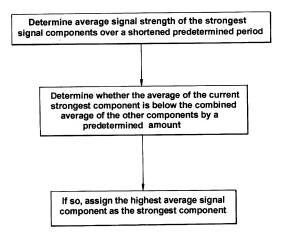


Fig. 5

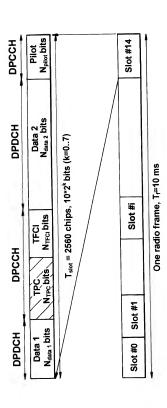
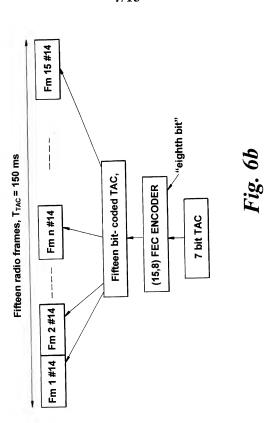


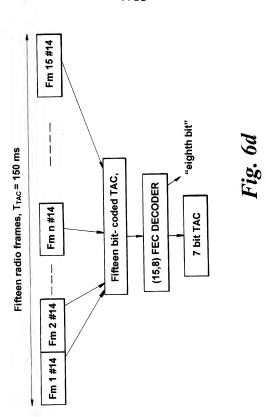
Fig. 6a

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7 hit TAC	finction
00000	
0000000	do nothing
1000000	if this is the only weighted bit set -
	advance time by table entry 0 (default 1/8 chip, tracking mode)
	if this is set in combination with other weighted bits -
	advance time by table entry 1 (default 1/4 microsecond)
0100000	advance time by table entry 2 (default 1/2 microsecond).
0000000	advance time by table entry 3 (default 1 microsecond)
0001000	advance time by table entry 4 (default 2 microseconds)
0000100	advance time by table entry 5 (default 4 microseconds)
0100000	advance time by table entry 6 (default 8 microseconds)
1000000	do nothing
1000001	if this is the only weighted bit set -
	retard time by table entry 0 (default 1/8 chip. tracking mode)
	if this is set in combination with other weighted bits -
	retard time by table entry 1 (default 1/4 microsecond)
1000010	retard time by table entry 2 (default 1/2 microsecond)
1000100	retard time by table entry 3 (default 1 microsecond)
1001000	retard time by table entry 4 (default 2 microseconds)
1010000	retard time by table entry 5 (default 4 microseconds)
110000	retard time by table entry 6 (default 8 microseconds)

Fig. 6c



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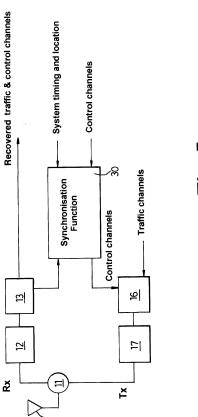
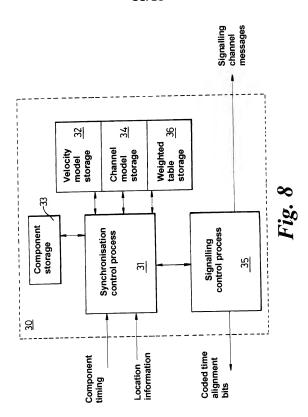


Fig. 7

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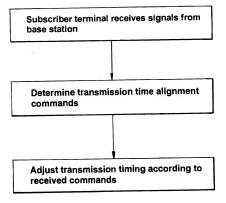


Fig. 9

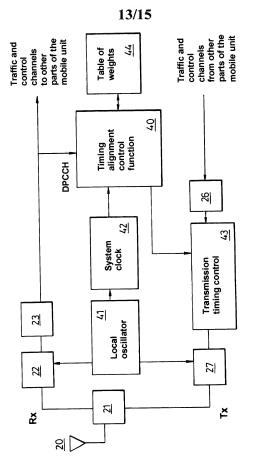
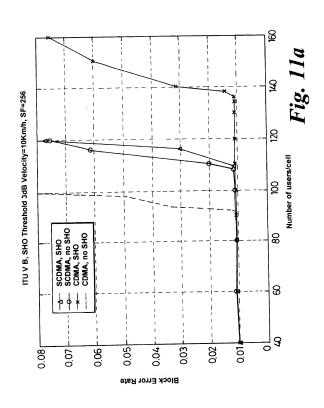


Fig. 10

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